12-W, 3.3/5-V INPUT, WIDE OUTPUT ADJUSTABLE BOOST CONVERTER

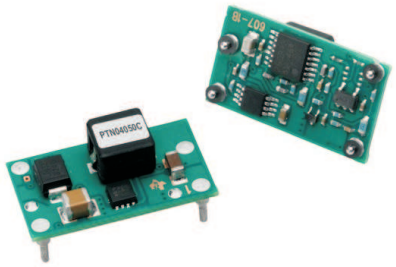
Check for Samples: PTN04050C

FEATURES

- **Up to 12 W Output Power**
- **Wide Input Voltage Range (2.95 V to 5.5 V)**
- **Wide Output Voltage Adjust (5 V to 15 V)**
- **High Efficiency (Up to 90%)**
- **Operating Temperature: –40°C to 85°C**
- **Surface Mount Package Available**

APPLICATIONS

- **Telecommunications, Instrumentation, and General-Purpose Applications**

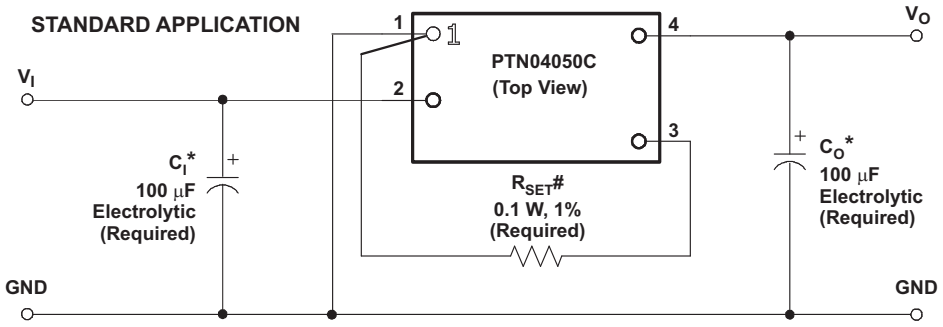


DESCRIPTION

The PTN04050C is a 4-pin boost-voltage regulator product. In new designs it should be considered in place of the PT5040 series of positive step-up products. The PTN04050C is smaller and lighter than its predecessors, and has either similar or improved electrical performance characteristics. The case-less, double-sided package, also exhibits improved thermal characteristics, and is compatible with TI's roadmap for RoHS and lead-free compliance.

Operating over a 2.95V to 5.5V input range, the PTN04050C provides high-efficiency, step-up voltage conversion for loads of up to 12W. The output voltage is set using a single external resistor. The PTN04050C may be set to any value within the range, 5V to 15V. The output voltage of the PTN04050C can be as little as 0.5V higher than the input, allowing an output voltage of 5V, with an input voltage of 4.5V.

The PTN04050C modules are suited to a wide variety of general-purpose applications that operate off 3.3-V or 5-V dc power.



* See the *Application Information* section for capacitor recommendations.
\# See the *Application Information* section for R_{SET} values.

These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

ORDERING INFORMATION

For the most current package and ordering information, see the Package Option Addendum at the end of this datasheet, or see the TI website at www.ti.com.

ABSOLUTE MAXIMUM RATINGS (1)

over operating free-air temperature range unless otherwise noted
all voltages with respect to GND (pin 1),

			UNIT
T_A	Operating free-air temperature	Over V_I range	–40°C to 85°C
	Leaded temperature (H suffix)	5 seconds	260°C (2)
	Solder reflow temperature (S suffix)	Surface temperature of module body or pins	235°C
	Solder reflow temperature (Z suffix) (3)	Surface temperature of module body or pins	260°C (3)
T_{stg}	Storage temperature		–55°C to 125°C
P_O	Output power		12 W

(1) Stresses beyond those listed under absolute maximum ratings may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under recommended operating conditions is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
(2) This model is NOT compatible with surface-mount reflow solder process.
(3) Moisture Sensitivity Level (MSL) Rating Level-3-260C-168HR

RECOMMENDED OPERATING CONDITIONS

		MIN	MAX	UNIT
V_I	Input voltage	2.95	5.5	V
T_A	Operating free-air temperature	–40	85	°C

PACKAGE SPECIFICATIONS

PTN04050Cx (Suffix AH, AS, and AZ)			
Weight			2.8 grams
Flammability	Meets UL 94 V-O		
Mechanical shock	Per Mil-STD-883D, Method 2002.3, 1 ms, 1/2 sine, mounted		500 G (1)
Mechanical vibration	Mil-STD-883D, Method 2007.2, 20-2000 Hz	Horizontal T/H (suffix AH)	20 G (1)
		Horizontal SMD (suffix AS and AZ)	15 G (1)

(1) Qualification limit.

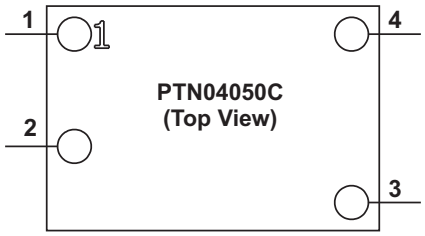
ELECTRICAL CHARACTERISTICS

operating at 25°C free-air temperature, V_I = 5 V, V_O = 12 V, I_O = I_O (max), C_I = 100 µF, C_O = 100 µF (unless otherwise noted)

PARAMETER		TEST CONDITIONS		PTN04050C			
				MIN	TYP	MAX	UNIT
I_O	Output current	Over V_I Range	V_O = 15 V	0.1 (1)		0.8	A
			V_O = 12 V	0.1 (1)		1.0	
			V_O = 9 V	0.1 (1)		1.3	
			V_O = 5 V	0.1 (1)		2.4	
V_I	Input voltage range	Over I_O range		2.95		5.5 (2)	V
V_O	Output adjust range			5		15	V
	Set-point voltage tolerance	T_A = 25°C				±2 (3)	%V_O
	Temperature variation	–40°C to 85°C			±0.5		%V_O
	Line regulation	Over V_I range				±0.5	%V_O
	Load regulation	Over I_O range				±0.5	%V_O
	Total Output Voltage Variation	Includes set point, line, load –40°C < T_A < 85°C				±3 (3)	%V_O
η	Efficiency		V_I = 5 V, R_{SET} = 60.4 Ω, V_O = 15 V		88%		
			V_I = 5 V, R_{SET} = 1.33 kΩ, V_O = 12 V		89%		
			V_I = 5 V, R_{SET} = 4.53 kΩ, V_O = 9 V		90%		
			V_I = 3.3 V, R_{SET} = OPEN, V_O = 5 V		87%		
	Output voltage ripple (peak-to-peak)	20-MHz bandwith			1.5	3	%V_O
	Transient response	1 A/µs load step from 50% to 100% $I_O$max					
			Recovery time		500		µs
			V_O over/undershoot		2.5		%V_O
I_{lim}	Current limit				150 (4)		%$I_O$max
I_{ir}	Inrush current				2 (5)		A
t_{ir}	Inrush current time duration				1		ms
F_S	Switching frequency	Over V_I and I_O ranges		450	525	600	kHz
C_I	External input capacitance			100 (6)			µF
C_O	External output capacitance	Nonceramic		100 (7)		560 (8)	µF
		Ceramic		0		100 (9)	
		Equivalent series resistance (nonceramic)		10 (10)			mΩ
MTBF	Calculated reliability	Per Telcordia SR-332, 50% stress, T_A = 40°C, ground benign		8.9			10^6 Hr

(1) Operation at no load is not recommended.
(2) The maximum V_I is 5.5V or (V_O- 0.5V) whichever is less. If the difference in V_O to V_{IN} is ≥ 0.5V and ≤ 1.4V, the device will operate in asynchronous mode. In this condition, there may be multiple output voltage ripple frequencies and the total output voltage variation may increase by up to 2%.
(3) The set-point voltage tolerance is affected by the tolerance and stability of R_{SET}. The stated limit is unconditionally met if R_{SET} has a tolerance of 1% with 100 ppm/°C or better temperature stability.
(4) Boost-topology switching regulators are not short-circuit protected.
(5) The inrush current stated is in addition to the normal input current for the associated output load.
(6) An external input capacitor is required across the input (V_I and GND) for proper operation. See the application information for further guidance.
(7) An external output capacitance is required for proper operation. See the application information for further guidance.
(8) The minimum ESR limitation may result in a lower value for the output capacitance. See the application information for further guidance.
(9) When using ceramic capacitors equivalent to 100 µF, a 100 µF bulk electrolytic is also required.
(10) This is the minimum ESR for all the electrolytic (nonceramic) output capacitance. Use 17 mΩ as the minimum when using maximum ESR values to calculate.

PIN ASSIGNMENT



PIN FUNCTIONS

PIN		I/O	DESCRIPTION
NAME	NO.		
GND	1	I/O	This is the common ground connection for the V_I and V_O power connections. It is also the 0 V_{dc} reference for the V_O *Adjust* control input.
V_I	2	I	The positive input voltage power node to the module, which is referenced to common GND.
V_O Adjust	3	I	A 1% resistor must be connected between this pin and GND (pin 1) to set the output voltage. If left open-circuit, the output voltage will default to its minimum adjust value. The temperature stability of the resistor should be 100 ppm/°C (or better). The set-point range is 5 V to 15 V. The standard resistor value for a number of common output voltages is provided in the application information.
V_O	4	O	The regulated positive power output with respect to the GND node.

TYPICAL CHARACTERISTICS (3.3-V INPUT)(1) (2)

EFFICIENCY VS OUTPUT CURRENT

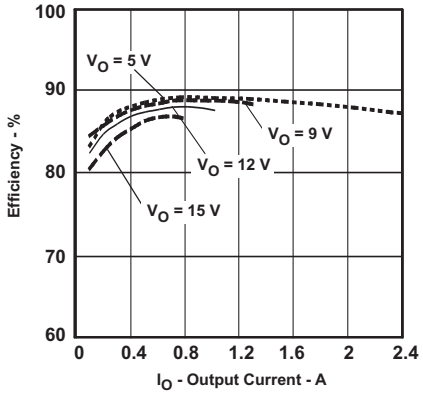


Figure 1.

OUTPUT VOLTAGE RIPPLE VS OUTPUT CURRENT

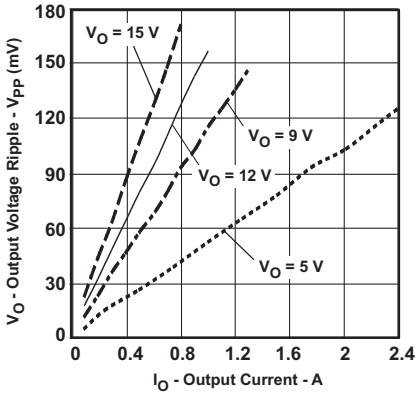


Figure 2.

POWER DISSIPATION VS OUTPUT CURRENT

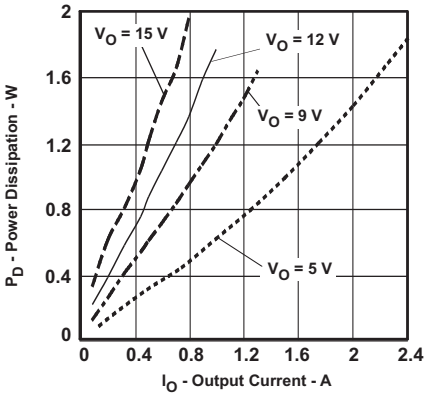


Figure 3.

TEMPERATURE DERATING VS OUTPUT CURRENT

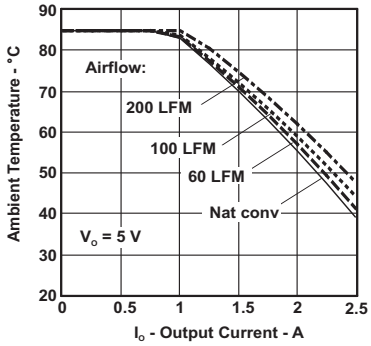


Figure 4.

TEMPERATURE DERATING VS OUTPUT CURRENT

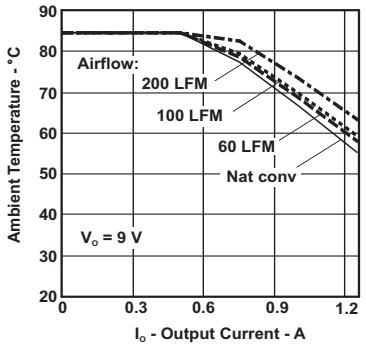


Figure 5.

TEMPERATURE DERATING VS OUTPUT CURRENT

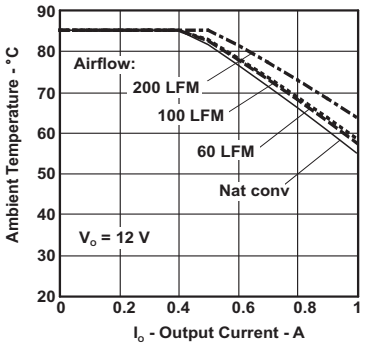


Figure 6.

TEMPERATURE DERATING VS OUTPUT CURRENT

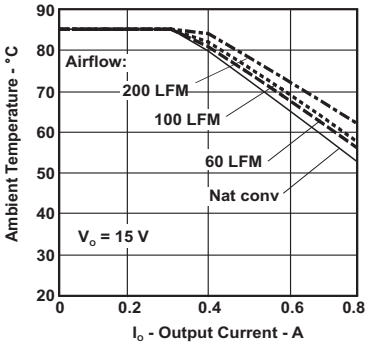


Figure 7.

(1) The electrical characteristic data has been developed from actual products tested at 25°C. This data is considered typical for the converter. Applies to Figure 1, Figure 2, and Figure 3.

(2) The Safe Operating Area curves represent the conditions at which internal components are at or below the manufacturer's maximum operating temperatures. Derating limits apply to modules soldered directly to a 100 mm x 100 mm double-sided PCB with 2 oz. copper. Applies to Figure 4, Figure 5, Figure 6, and Figure 7.

TYPICAL CHARACTERISTICS (5-V INPUT)(1) (2)

EFFICIENCY VS OUTPUT CURRENT

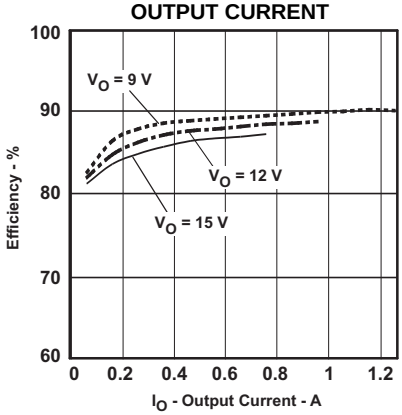


Figure 8.

OUTPUT VOLTAGE RIPPLE VS OUTPUT CURRENT

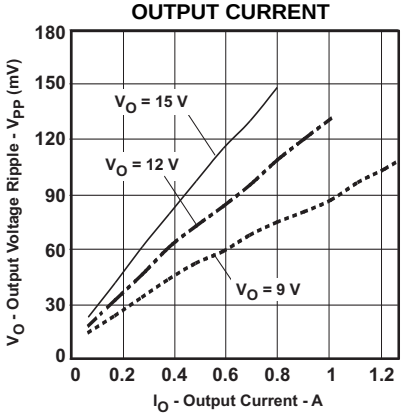


Figure 9.

POWER DISSIPATION VS OUTPUT CURRENT

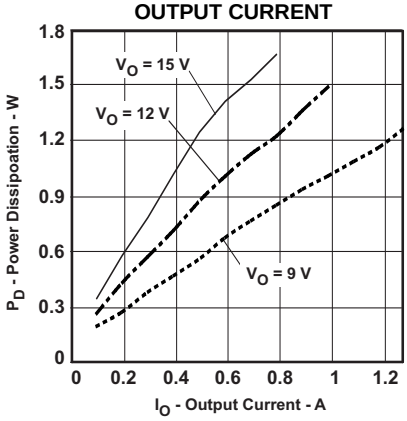


Figure 10.

TEMPERATURE DERATING VS OUTPUT CURRENT

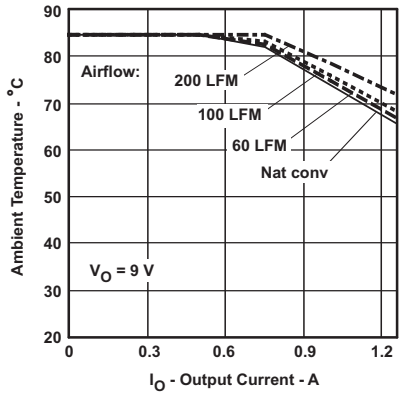


Figure 11.

TEMPERATURE DERATING VS OUTPUT CURRENT

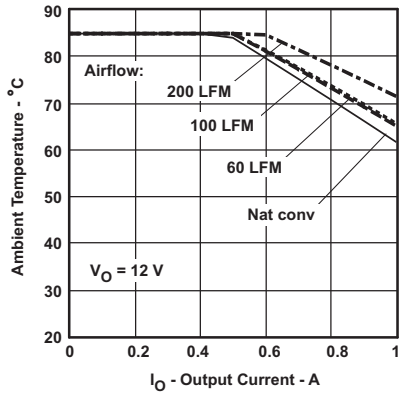


Figure 12.

TEMPERATURE DERATING VS OUTPUT CURRENT

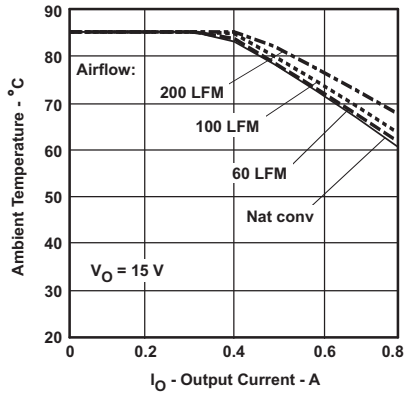


Figure 13.

(1) The electrical characteristic data has been developed from actual products tested at 25°C. This data is considered typical for the converter. Applies to Figure 8, Figure 9, and Figure 10.

(2) The Safe Operating Area curves represent the conditions at which internal components are at or below the manufacturer's maximum operating temperatures. Derating limits apply to modules soldered directly to a 100-mm x 100-mm, double-sided PCB with 2 oz. copper. Applies to Figure 11, Figure 12, and Figure 13.

APPLICATION INFORMATION

Adjusting the Output Voltage of the PTN04050C Wide-Output Adjust Power Modules

General

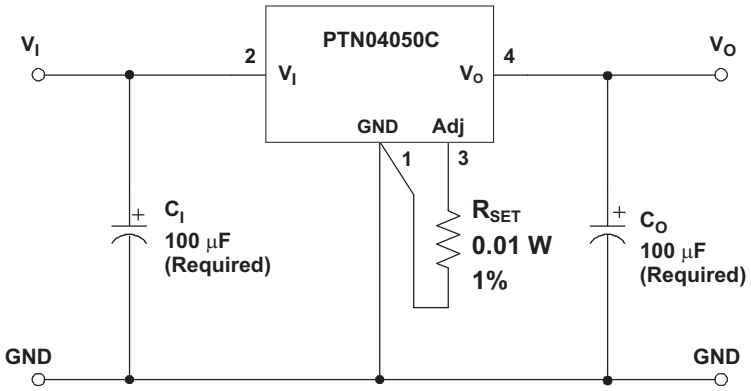
A resistor must be connected between the *V_O Adjust* control (pin 3) and *GND* (pin 1) to set the output voltage of the PTN04050C product. The adjustment range is from 5 V to 15 V. If pin 3 is left open, the output voltage defaults to the lowest value.

Table 1 gives the standard resistor value for several common voltages, along with the actual output voltage that the value provides. For other output voltages, the value of the required resistor can be calculated using Equation 1. Alternatively, R_{SET} can be simply selected from the range of values given in Table 2. Figure 14 shows the placement of the required resistor.

$$R_{SET} = 15\ k\Omega \times \frac{2\ V}{V_O - 5\ V} - 2.94\ k\Omega \quad (1)$$

Table 1. Standard Values of R_{SET} for Common Output Voltages

V_O (Required)	R_{SET} (Standard Value)	V_O (Actual)
5.0 V	Open	5.00 V
9.0 V	4.53 kΩ	9.01 V
12.0 V	1.33 kΩ	12.03 V
15.0 V	60.4 Ω	14.99 V



(1) A 0.05-W rated resistor may be used. The tolerance should be 1%, with a temperature stability of 100 ppm/°C (or better). Place the resistor as close to the regulator as possible. Connect the resistor directly between pins 3 and 1 using dedicated PCB traces.

(2) Never connect capacitors from *V_O Adjust* to *GND* or *V_O* . Any capacitance added to the *V_O Adjust* pin affects the stability of the regulator.

Figure 14. PTN04050C V_O Adjust Resistor Placement

Table 2. PTN04050C Output Voltage Set-Point Resistor Values

V_O	R_{SET}	V_O	R_{SET}	V_O	R_{SET}
5 V	Open	10 V	3.06 kΩ	12.6 V	1.01 kΩ
5.2 V	147 kΩ	10.1 V	2.94 kΩ	12.7 V	956 Ω
5.4 V	72 kΩ	10.2 V	2.83 kΩ	12.8 V	906 Ω
5.6 V	47 kΩ	10.3 V	2.72 kΩ	12.9 V	857 Ω
5.8 V	34.5 kΩ	10.4 V	2.62 kΩ	13 V	810 Ω
6 V	27 kΩ	10.5 V	2.52 kΩ	13.1 V	764 Ω
6.2 V	22 kΩ	10.6 V	2.42 kΩ	13.2 V	719 Ω
6.4 V	18.5 kΩ	10.7 V	2.32 kΩ	13.3 V	674 Ω
6.6 V	15.8 kΩ	10.8 V	2.23 kΩ	13.4 V	631 Ω
6.8 V	13.7 kΩ	10.9 V	2.15 kΩ	13.5 V	589 Ω
7 V	12 kΩ	11 V	2.06 kΩ	13.6 V	548 Ω
7.2 V	10.7 kΩ	11.1 V	1.98 kΩ	13.7 V	508 Ω
7.4 V	9.56 kΩ	11.2 V	1.89 kΩ	13.8 V	469 Ω
7.6 V	8.60 kΩ	11.3 V	1.82 kΩ	13.9 V	431 Ω
7.8 V	7.77 kΩ	11.4 V	1.75 kΩ	14 V	393 Ω
8 V	7.06 kΩ	11.5 V	1.67 kΩ	14.1 V	357 Ω
8.2 V	6.44 kΩ	11.6 V	1.60 kΩ	14.2 V	321 Ω
8.4 V	5.88 kΩ	11.7 V	1.54 kΩ	14.3 V	286 Ω
8.6 V	5.39 kΩ	11.8 V	1.47 kΩ	14.4 V	251 Ω
8.8 V	4.95 kΩ	11.9 V	1.41 kΩ	14.5 V	218 Ω
9 V	4.56 kΩ	12 V	1.35 kΩ	14.6 V	185 Ω
9.2 V	4.20 kΩ	12.1 V	1.29 kΩ	14.7 V	153 Ω
9.4 V	3.88 kΩ	12.2 V	1.23 kΩ	14.8 V	121 Ω
9.6 V	3.58 kΩ	12.3 V	1.17 kΩ	14.9 V	90 Ω
9.8 V	3.31 kΩ	12.4 V	1.11 kΩ	15 V	60 Ω
9.9 V	3.18 kΩ	12.5 V	1.06 kΩ		

CAPACITOR RECOMMENDATIONS FOR PTN04050C WIDE-OUTPUT ADJUST POWER MODULES

Input Capacitor

The minimum required input capacitance is 100 µF. The minimum ripple current rating for any nonceramic capacitance must be greater than 250 mA rms. The ripple current rating of electrolytic capacitors is a major consideration when they are used at the input. This ripple current requirement can be reduced by placing ceramic capacitors at the input, in addition to the minimum required capacitance.

When specifying regular tantalum capacitors for use at the input, a minimum voltage rating of 2 X (maximum dc voltage + ac ripple) is highly recommended. This is standard practice to ensure reliability. Polymer-tantalum capacitors are not affected by this requirement. (Please verify voltage derating for the polymer-tantalum capacitors from the vendors.)

Output Capacitor

The minimum capacitance required to insure stability is a 100 µF. A combination of both ceramic and electrolytic-type capacitors should be used. The minimum ripple current rating for the nonceramic capacitance must be at least 150 mA rms. When using ceramic capacitors equivalent to 100 µF, a 100 µF bulk electrolytic is also required. The stability of the module and voltage tolerances are compromised if the capacitor is not placed near the output pin. A high-quality, computer-grade electrolytic capacitor is adequate. Ceramic capacitance should also be located within 0.5 inches (1,27 cm) of the output pin.

For applications with load transients (sudden changes in load current), the regulator response improves with additional capacitance. Additional electrolytic capacitors should be located close to the load circuit. These capacitors provide decoupling over the frequency range, 2 kHz to 150 kHz. Aluminum electrolytic capacitors are suitable for ambient temperatures above 0°C. For operation below 0°C, tantalum or OS-CON type capacitors are recommended. When using one or more nonceramic capacitors, the calculated equivalent ESR should be no lower than 10 mΩ (17 mΩ using the manufacturer's maximum ESR for a single capacitor). A list of capacitors and vendors are identified in Table 3, the recommended capacitor table.

Ceramic Capacitors

Above 150 kHz the performance of aluminum electrolytic capacitors becomes less effective. To further reduce the reflected input ripple current, or the output transient response, multilayer ceramic capacitors must be added. Ceramic capacitors have low ESR and their resonant frequency is higher than the bandwidth of the regulator. When placed at the output, their combined ESR is not critical as long as the total value of ceramic capacitance does not exceed 100 µF.

Note: If only ceramics are used on the output bus, then a 100 µF electrolytic is required for stabilization.

Tantalum Capacitors

Tantalum type capacitors may be used at the output, and are recommended for applications where the ambient operating temperature can be less than 0°C. The AVX TPS, Sprague 593D/594/595, and Kemet T495/T510/T520 capacitors series are suggested over many other tantalum types due to their rated surge, power dissipation, and ripple current capability. As a caution, many general-purpose tantalum capacitors have considerably higher ESR, reduced power dissipation, and lower ripple current capability. These capacitors are also less reliable as they have lower power dissipation and surge current ratings. Tantalum capacitors that do not have a stated ESR or surge current rating are not recommended for power applications. When specifying OS-CON and polymer tantalum capacitors for the output, the minimum ESR limit is encountered well before the maximum capacitance value is reached.

Capacitor Table

The capacitor table, Table 3, identifies the characteristics of capacitors from various vendors with acceptable ESR and ripple current (rms) ratings. The recommended number of capacitors required at both the input and output buses is identified for each capacitor type. This is not an extensive capacitor list. Capacitors from other vendors are available with comparable specifications. Those listed are for guidance. The rms current rating and ESR (at 100 kHz) are critical parameters necessary to insure both optimum regulator performance and long capacitor life.

Designing for Load Transients

The transient response of the dc/dc converter has been characterized using a load transient with a di/dt of 1 A/µs. The typical voltage deviation for this load transient is given in the data sheet specification table using the required value of output capacitance. As the di/dt of a transient is increased, the response of a converter's regulation circuit ultimately depends on its output capacitor decoupling network. This is an inherent limitation of any dc/dc converter once the speed of the transient exceeds its bandwidth capability. If the target application specifies a higher di/dt or lower voltage deviation, the requirement can only be met with additional output capacitor decoupling. In these cases, special attention must be paid to the type, value, and ESR of the capacitors selected.

If the transient performance requirements exceed those specified in the data sheet, the selection of output capacitors becomes more important. Obey the minimum ESR and maximum capacitance limits specified in the Electrical Characteristics table.

Table 3. Recommended Input/Output Capacitors (1)

CAPACITOR VENDOR/ COMPONENT SERIES	CAPACITOR CHARACTERISTICS					QUANTITY		VENDOR NUMBER
	WORKING VOLTAGE (V)	VALUE (µF)	EQUIVALENT SERIES RESISTANCE (ESR) (Ω)	85°C MAXIMUM RIPPLE CURRENT (I_{rms}) (mA)	PHYSICAL SIZE (mm)	INPUT BUS	OUTPUT BUS	
Panasonic FC(Radial)	25	180	0.117	555	8 X 11	1	1	EEUFC1E181
Panasonic FC (SMD)	25	100	0.30	450	8 X 10,2	1	1	EEVFC1E101P
United Chemi-Con PXA (SMD)	16	150	0.026	3430	10 X 7,7	1	1	PXA16VC151MJ80TP (V_O≤13V)
PS	25	100	0.020	4320	10 X 12,5	1	1	25PS100MJ12
LXZ	25	100	0.250	290	6,3 X 11,5	1	1	LXZ25VB101M6X11LL
MVY(SMD)	35	100	0.300	450	8 X 10	1	1	MVY35VC101MH10TP
Nichicon UWG (SMD)	50	100	0.300	500	10 X 10	1	1	UWG1H101MNR1GS
F559 (Tantalum)	10	100	0.055	2000	7,7 X 4,3	1	1 (2)	F551A107MN (V_O≤ 5V)
HD	25	100	0.130	405	6,3 X 11	1	1	UHD1E101MER
Sanyo OS-CON SVP (SMD)	20	100	0.024	2500	8 X 12	1	1	20SVP100M
SP	16	100	0.032	2890	10 X 5	1	1 (2)	16SP100M (V_O≤ 14V)
AVX Tantalum TPS (SMD)	20	100	0.085	1543	7,3X 6,1X 3.5	1	1 (2)	TPSV107M020R0085 (V_O ≤ 10 V)
	20	100	0.200	> 817	7,3X 6,1X 3.5	1	1 (2)	TPSV107M020R0200 (V_O ≤ 10 V)
Murata X5R Ceramic	6.3	100	0.002	>1000	3225	1	1 (2)	GRM32ER60J107M (V_O ≤ 5.5 V)
TDK X5R Ceramic	6.3	100	0.002	>1000	3225	1	1 (2)	C3225X5R0J107MT (V_O ≤ 5.5 V)
Murata X5R Ceramic	16	47	0.002	>1000	3225	2	≤2 (2)	GRM32ER61C476M
Kemet X5R Ceramic	6.3	47	0.002	>1000	3225	2	≤2 (2)	C1210C476K9PAC (V_O ≤ 5.5 V)
TDK X5R Ceramic	6.3	47	0.002	>1000	3225	2	≤2 (2)	C3225X5R0J476MT (V_O ≤ 5.5 V)
Murata X5R Ceramic	6.3	47	0.002	>1000	3225	2	≤2 (2)	GRM422X5R476M6.3 (V_O ≤ 5.5 V)
TDK X5R Ceramic	16	22	0.002	>1000	3225	5	≤5 (2)	C3225X5R1E2265KT/MT
Murata X7R Ceramic	25	22	0.002	>1000	3225	5	≤5	GRM32ER61C226K
Kemet X7R Ceramic	16	22	0.002	>1000	3225	5	≤5 (2)	C1210C226K3PAC

(1) **Capacitor Supplier Verification**
1. Verify availability of capacitors identified in this table. Capacitor suppliers may recommend alternative part numbers because of limited availability or obsolete products. In some instances, the capacitor product life cycle may be in decline and have short-term consideration for obsolescence.
RoHS, Lead-free and Material Details
2. Consult capacitor suppliers regarding material composition, RoHS status, lead-free status, and manufacturing process requirements. Component designators or part number deviations may occur if material composition or soldering requirements change.

(2) The maximum voltage rating of the capacitor must be selected for the desired set-point voltage (V_O). To operate at a higher output voltage, select a capacitor with a higher voltage rating.

Power-Up Characteristics

When configured per the standard application, the PTN04050C power module produces a regulated output voltage following the application of a valid input source voltage. During power up, internal soft-start circuitry slows the rate that the output voltage rises, thereby limiting the amount of in-rush current drawn from the input source.Figure 15 shows the power-up waveforms for a PTN04050C, operating from a 5-V input and with the output voltage adjusted to 12 V. The waveforms were measured with a 1-A resistive load.

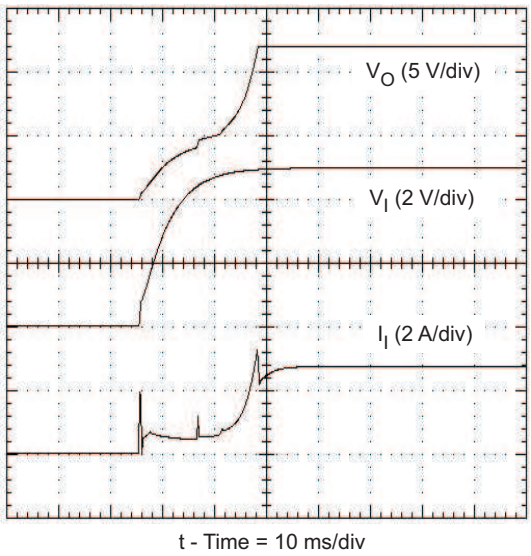


Figure 15. Power-Up Waveforms

Overtemperature Protection

A thermal shutdown mechanism protects the module's internal circuitry against excessively high temperatures. A rise in temperature may be the result of a drop in airflow, a high ambient temperature, or a sustained overcurrent condition. If the junction temperature of the internal control IC rises excessively, the module turns its boost operation off. Although the module is off, an output voltage of approximately (V_I – 300 mV) is still present. The module restarts boost operation when the sensed temperature decreases by approximately 10 degrees.

Note: *Overtemperature protection is a last resort mechanism to prevent damage to the module. It should not be relied on as permanent protection against thermal stress. Always operate the module within its temperature derated limits, for the worst-case operating conditions of output current, ambient temperature, and airflow. Operating the module above these limits, albeit below the thermal shutdown temperature, reduces the long-term reliability of the module.*

Boost Topology

With boost regulators an output voltage of approximately (V_I - 300 mV) is present whenever the input voltage to the module is below the minimum input voltage range, or during an overtemperature condition. Also, a boost regulator cannot provide inherent short-circuit protection. This is due to the fact that there is a dc path from the input to the output even when the PWM and FET are not operating. This is shown in the boost topology diagram in Figure 16.

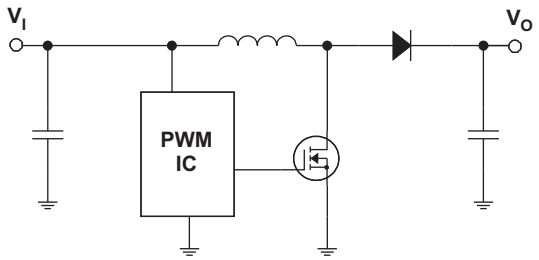


Figure 16. Typical Boost Converter Topology

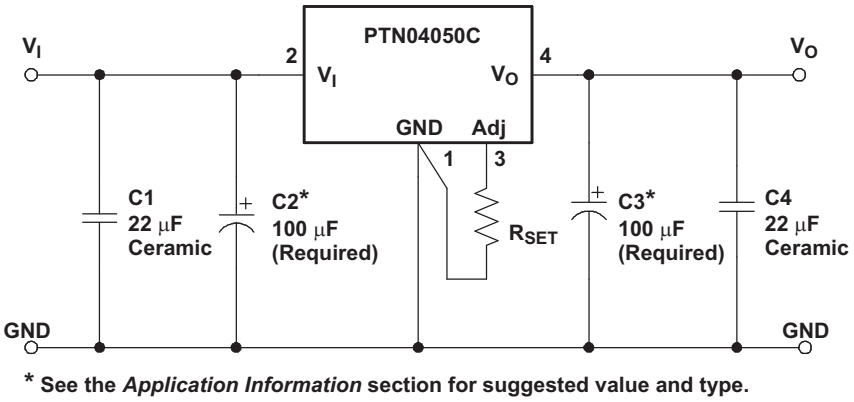
Optional Input/Output Filters

Power modules include internal input and output ceramic capacitors in all their designs. However, some applications require much lower levels of either input reflected or output ripple/noise. This section describes various filters and design techniques found to be successful in reducing both input and output ripple/noise.

Input/Output Capacitors

A first step toward reducing output ripple and noise is to add one or more 22-µF ceramic capacitors, such as C4 shown in Figure 17. Ceramic capacitors should be placed close to the output power terminals. A single 22-µF capacitor reduces the output ripple/noise by 10% to 30% for modules with a rated output current of less than 3 A. (**Note:** C3 is recommended to improve the regulators transient response and does not reduce output ripple and noise.)

Switching regulators draw current from the input line in pulses at their operating frequency. The amount of reflected (input) ripple/noise generated is directly proportional to the equivalent source impedance of the power source including the impedance of any input lines. The addition of C1, minimum 22-µF ceramic capacitor, near the input power pins, reduces reflected conducted ripple/noise by 30% to 50%.



* See the *Application Information* section for suggested value and type.

Figure 17. Adding High-Frequency Bypass Capacitors To The Input and Output

π Filters

If a further reduction in ripple/noise level is required for an application, higher order filters must be used. A π (pi) filter, employing a ferrite bead (Fair-Rite Part Number 2673000701 or equivalent) in series with the input or output terminals of the regulator reduces the ripple/noise by at least 20 db (see Figure 18 and Figure 19). In order for the inductor to be effective in reduction of ripple and noise, ceramic capacitors are required. (Note: for additional information on vendors and component suggestions, see the capacitor recommendations for the PTN04050C.)

These inductors plus ceramic capacitors form an excellent filter because of the rejection at the switching frequency (650 kHz - 1 MHz). The placement of this filter is critical. It must be located as close as possible to the input or output pins to be effective. The ferrite bead is small (12,5 mm X 3 mm) and has low dc resistance. Fair-Rite also manufactures a surface-mount bead (Part No. 2773021447), through hole (Part Number 2673000701) rated to 5 A. Inductors in the range of 1 µH to 5 µH can be used in place of the ferrite inductor bead.

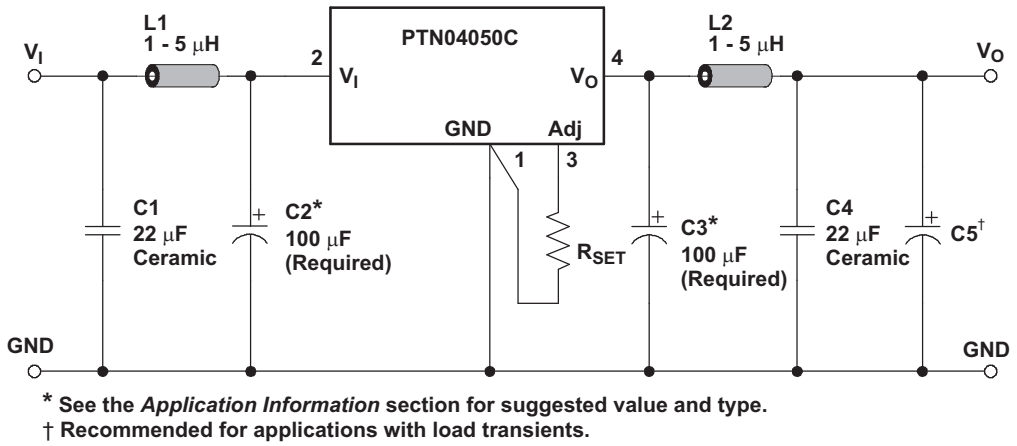


Figure 18. Adding π Filters

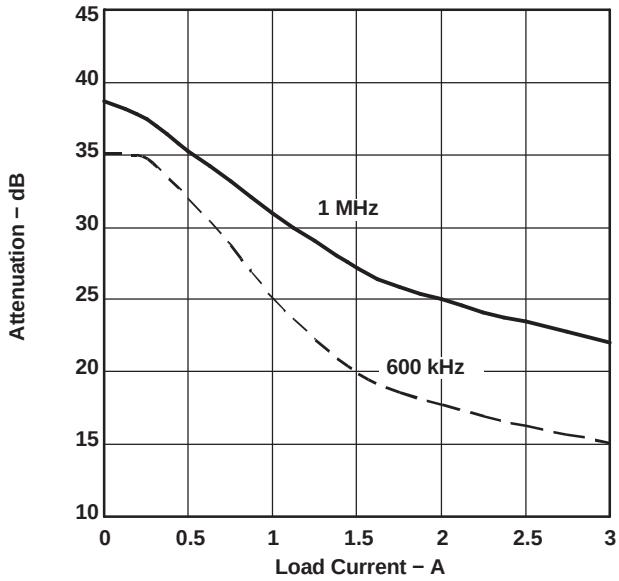


Figure 19. π-Filter Attenuation vs. Load Current

REVISION HISTORY

Changes from Original (September 2005) to Revision A **Page**

- Changed the Abs Max Ratings Storage temperature from: -40°C to 125°C To: -55°C to 125°C 2
- Changed Note 2 of the ELECTRICAL CHARACTERISTICS table 3

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
PTN04050CAD	ACTIVE	Through-Hole Module	EUU	4	56	RoHS (In Work) & Green (In Work)	SN	N / A for Pkg Type	-40 to 85		Samples
PTN04050CAH	ACTIVE	Through-Hole Module	EUU	4	56	RoHS (In Work) & Green (In Work)	SN	N / A for Pkg Type	-40 to 85		Samples
PTN04050CAS	ACTIVE	Surface Mount Module	EUV	4	56	Non-RoHS & Green (In Work)	SNPB	Level-1-235C-UNLIM/ Level-3-260C-168HRS	-40 to 85		Samples
PTN04050CAZ	ACTIVE	Surface Mount Module	EUV	4	56	RoHS (In Work) & Green (In Work)	SNAGCU	Level-3-260C-168 HR	-40 to 85		Samples
PTN04050CAZT	ACTIVE	Surface Mount Module	EUV	4	250	RoHS (In Work) & Green (In Work)	SNAGCU	Level-3-260C-168 HR	-40 to 85		Samples

(1) The marketing status values are defined as follows:
ACTIVE: Product device recommended for new designs.
LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.
NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.
PREVIEW: Device has been announced but is not in production. Samples may or may not be available.
OBSOLETE: TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".
RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.
Green: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

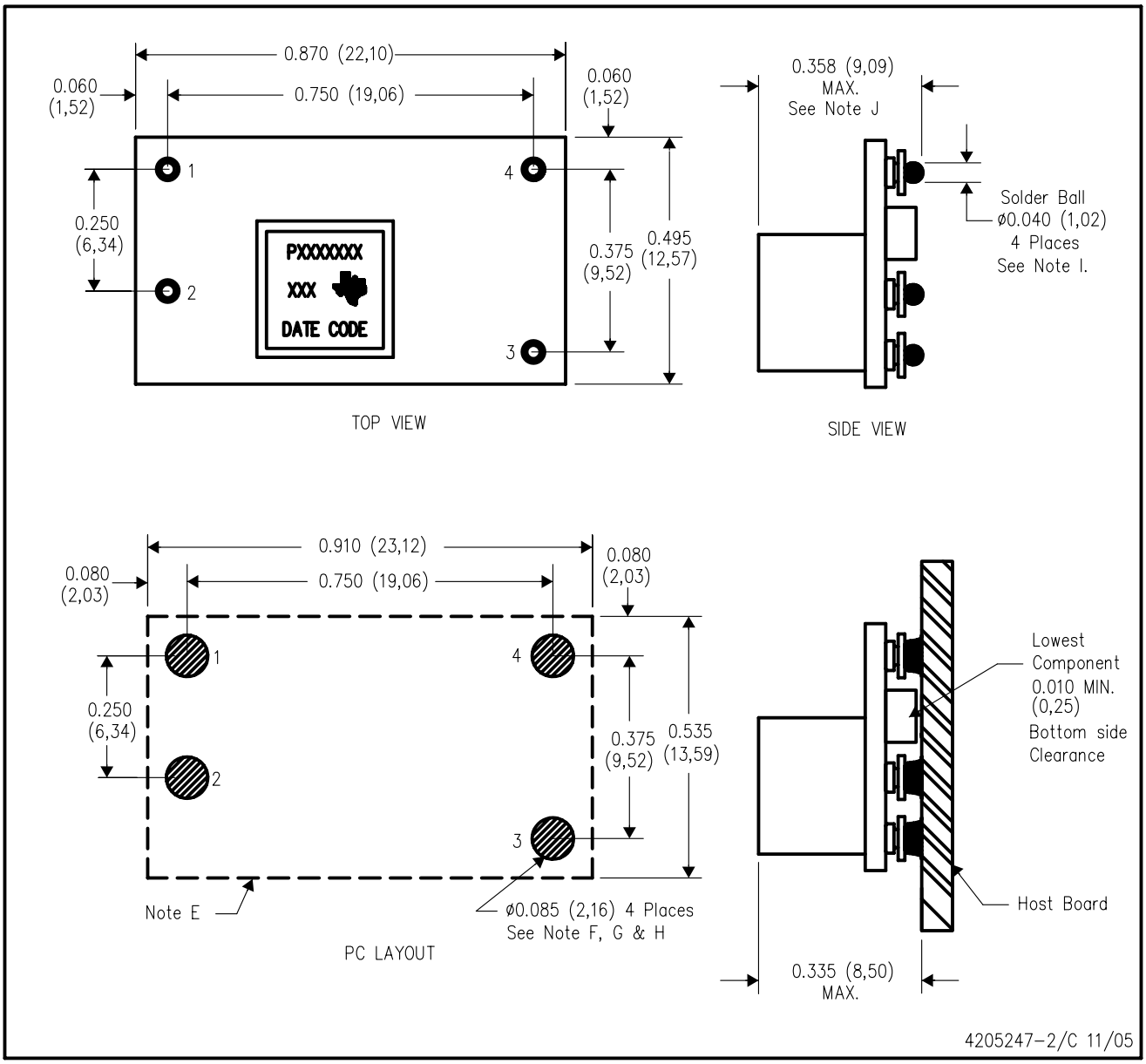
(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

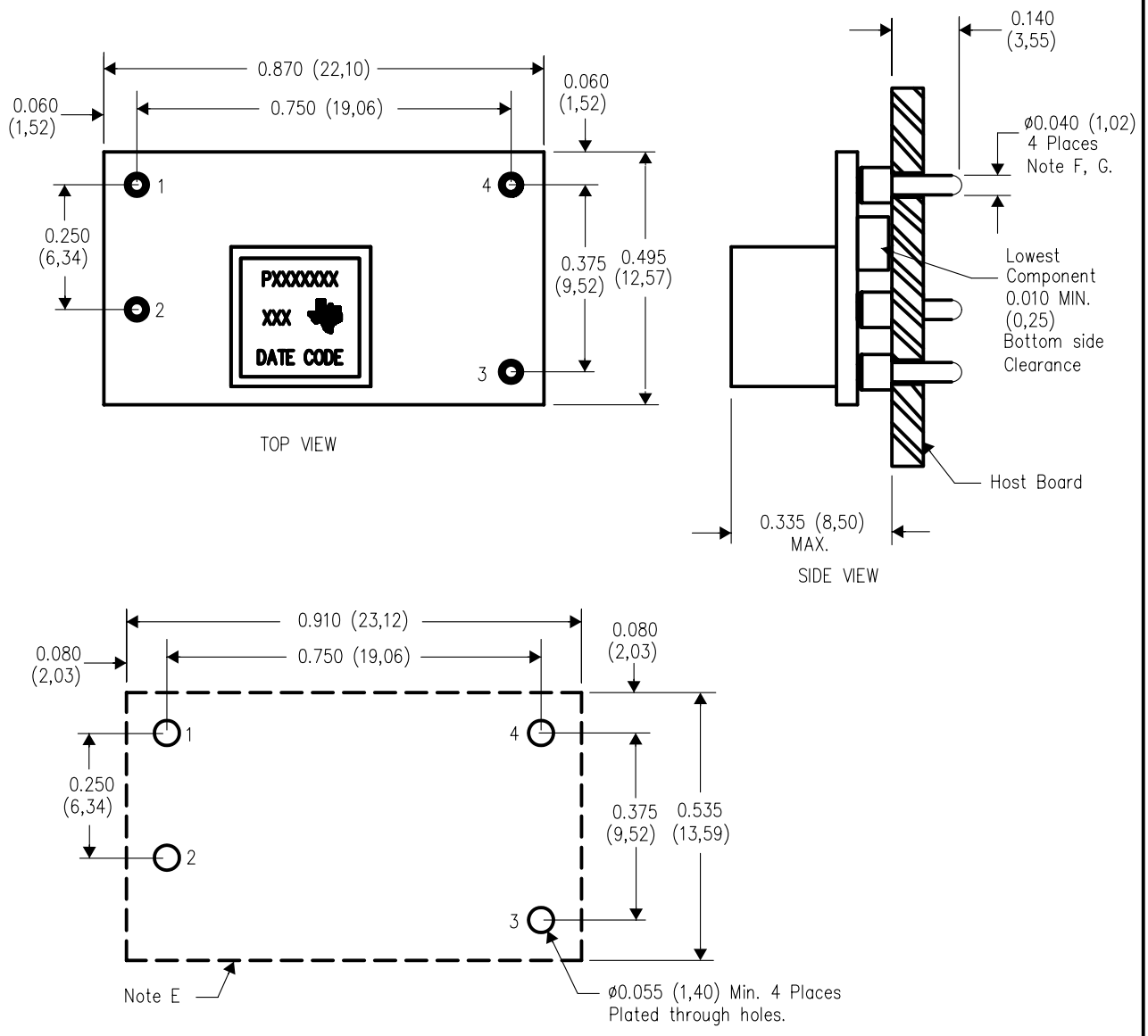
(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

EUV (R–PDSS–B4) DOUBLE SIDED MODULE



NOTES:
A. All linear dimensions are in inches (mm).
B. This drawing is subject to change without notice.
C. 2 place decimals are ±0.030 (±0,76mm).
D. 3 place decimals are ±0.010 (±0,25mm).
E. Recommended keep out area for user components.
F. Power pin connection should utilize two or more vias to the interior power plane of 0.025 (0,63) I.D. per input, ground and output pin (or the electrical equivalent).
G. Paste screen opening: 0.080 (2,03) to 0.085 (2,16). Paste screen thickness: 0.006 (0,15).
H. Pad type: Solder mask defined.
I. All pins: Material – Copper Alloy
Finish – Tin (100%) over Nickel plate
Solder Ball – See product data sheet.
J. Dimension prior to reflow solder.

EUU (R–PDSS–T4) DOUBLE SIDED MODULE



NOTES:
A. All linear dimensions are in inches (mm).
B. This drawing is subject to change without notice.
C. 2 place decimals are ±0.030 (±0,76mm).
D. 3 place decimals are ±0.010 (±0,25mm).
E. Recommended keep out area for user components.
F. Pins are 0.040" (1,02) diameter with 0.070" (1,78) diameter standoff shoulder.
G. All pins: Material – Copper Alloy
Finish – Tin (100%) over Nickel plate

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